

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Anthony J. Stratakos, et al.

Art Unit : Unknown

Serial No. : Unassigned

Examiner : Unknown

Filed : January 11, 2000

Title : DISCRETE-TIME SAMPLING OF DATA FOR USE IN SWITCHING
REGULATORSAssistant Commissioner for Patents
Washington, D.C. 20231

AIA
 Pre-allowance
 TCC
 SJC

PRELIMINARY AMENDMENTIn the Specification:

~~On page 1, line 1 of the application, please insert - This is a continuation application of~~
 U.S. Application Serial No. 08/991,394, filed December 16, 1997.
 A1 Now U.S. Pat. No. 6,020,729

In the Claims:

Please cancel claims 1-22 and add the following new claims:

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23. (New) A method of operating a voltage regulator having an input terminal to be coupled to an input voltage source and an output terminal to be coupled to a load, comprising:
 alternately coupling and decoupling the input terminal to the output terminal with a power switch;
 filtering a current between the input terminal and the output terminal to provide a substantially DC voltage at the output terminal;
 capturing a measurement of an electrical characteristic of the voltage regulator at a discrete moment of time with a sampling circuit;
 receiving the captured measurement with a feedback circuit coupled to the sampling circuit and the power switch; and
 using the measurement to control the power switch to maintain the DC voltage substantially constant.

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